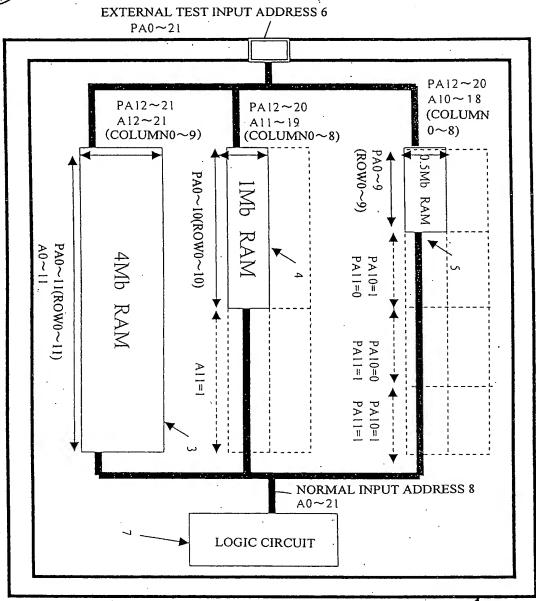


MEMORY EMBEDDED SYSTEM LSI 1







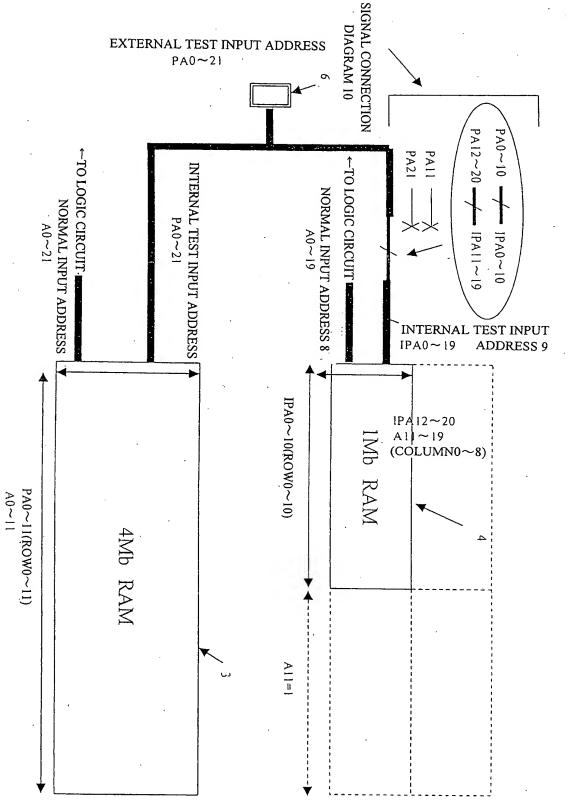
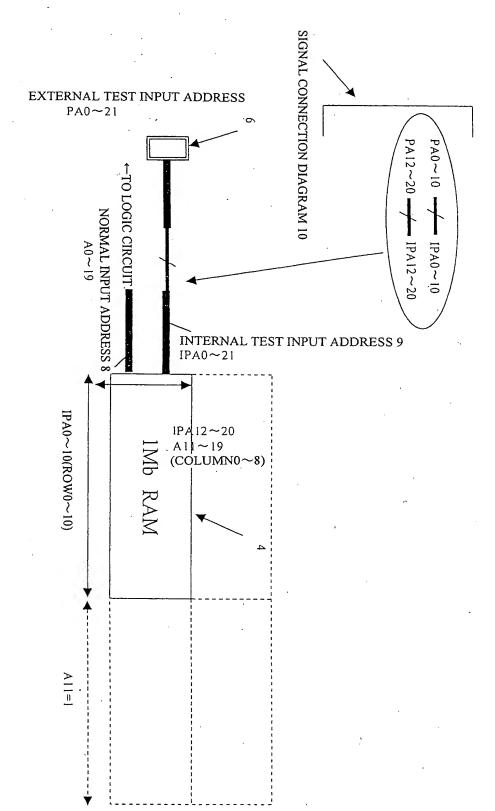
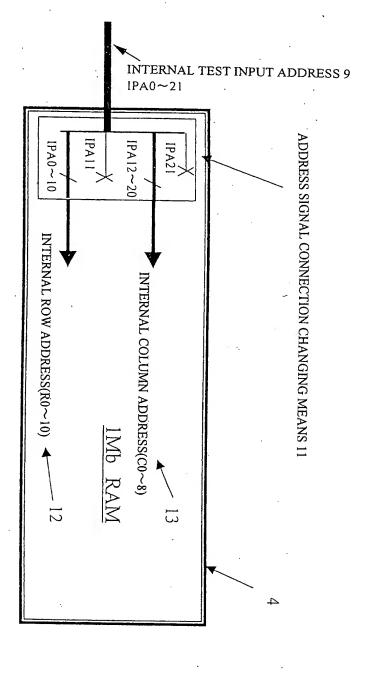


FIG. 3









1G. 5



FIG. 6

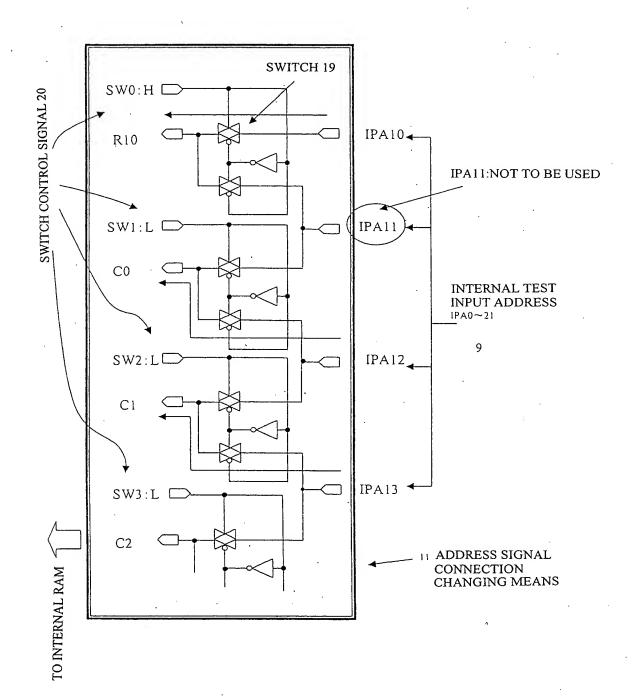
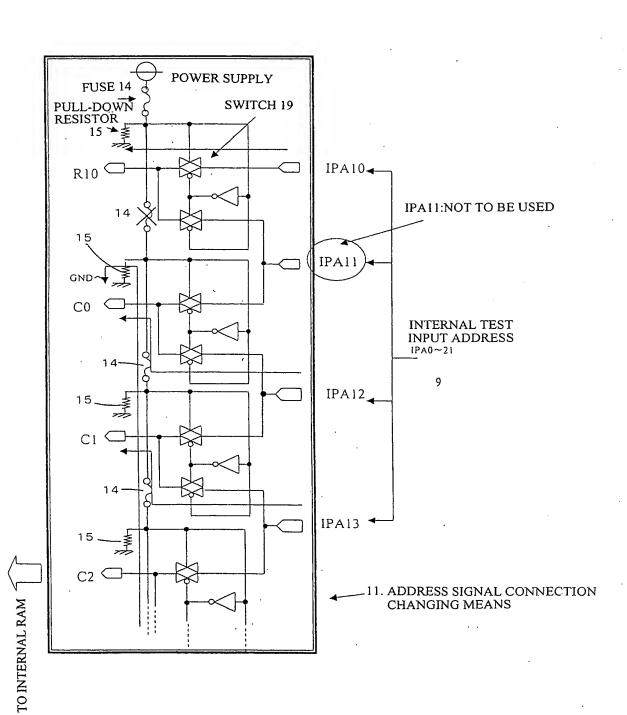
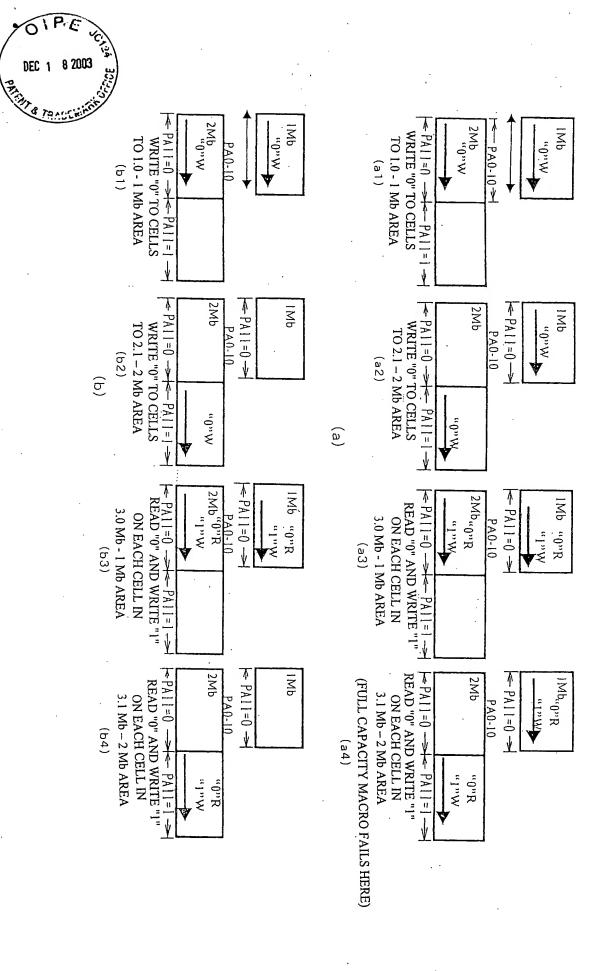


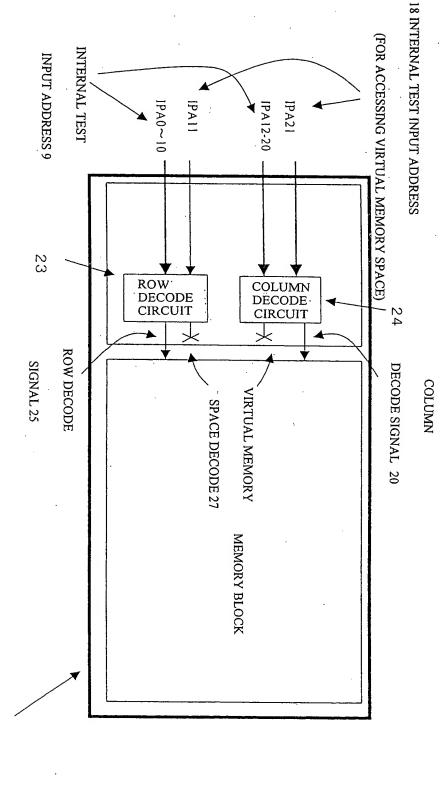


FIG. 7





HIG. 9



4. IMb RAM